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U.S. **UTILITY** Patent Application

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PATENT DATE

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APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER \
09/727424	F	700	28	2121	Perez JANIA
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Hirotaka Kaji Masashi Yamaguchi Hiroshi Harada Yukio Matsushita

TITLE

Apparatus for optimizing control system of unit device integrated in machine assembly

PATENT NUMBER

ISSUING CLASSIFICATION						
ORIGIN	AL	CROSS REFERENCE(S)				
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)			
INTERNATIONA	L CLASSIFICATION					
			☐ Continued on Issue Slip Inside File Jacket			

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
The term of this patent subsequent to(date)				NOTICE OF ALL	OWANCE MAILED
has been disclaimed.	(Assistant E	Examiner)	(Date)	ı	
☐ The term of this patent shall not extend beyond the expiration date					
of U.S Patent. No.	İ		l	ISSUE FEE	
			ſ	Amount Due	Date Paid
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The terminalmonths of this patent have been disclaimed.	}			ISSUE BATCH NUMBER	
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